## SKODENSHI AUK

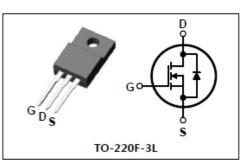
#### SWITCHING REGULATOR APPLICATIONS

#### Features

Type NO

- High Voltage : BV<sub>DSS</sub>=600V(Min.)
- Low Crss : Crss=14.6 pF(Typ.)
- Low gate charge : Qg=41nC(Typ.)
- Low  $R_{DS(on)}$ :  $R_{DS(on)}=0.65\Omega(Max.)$

### PIN Connection



**KMK1260F** 

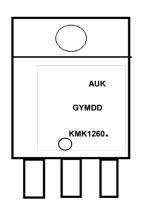
Advanced N-Ch Power MOSFET

# KMK1260F KMK1260. TO-220F-3L

Marking

#### **Marking Diagram**

**Ordering Information** 



• Da Lian

Column 1 : Manufacturer Column 2 : Production Information e.g.) GYMDD -. G : Factory management code -. YMDD : Date Code (year, month, date) Column 3 : Device Code

Package code

#### Absolute maximum ratings (Tc=25°C unless otherwise noted)

Characteristic	Symbol		R	ating	Unit
Drain-source voltage	VDSS		600		V
Gate-source voltage		VGSS ±30		-30	V
	T <sub>n</sub>	Tc=25℃		12	A
Drain current (DC) *	ID	Tc=100℃		7.1	A
Drain current (Pulsed)*		Idm		48	A
Power dissipation		Pd		45	W
Avalanche current (Single) 2		IAS		12	А
Single pulsed avalanche energy 2		Eas		549	mJ
Avalanche current (Repetitive) ①		Iar		12	А
Repetitive avalanche energy ①	Ear		Ear 11.6		mJ
Junction temperature		Tı		150	Ĉ
Storage temperature range	Tstg		-55	5~150	C

\* Limited by maximum junction temperature

## **KMK1260F**

Cha	racteristic	Symbol	Тур.	Max	Unit
Thermal resistance	Junction-case	Rth(J-C)	-	2.7	°C <b>/W</b>
	Junction-ambient	Rth(J-A)	-	62.5	°C <b>/W</b>

#### Electrical Characteristics (Tc=25°C unless otherwise noted)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Drain-source breakdown voltage	BVdss	ID=250uA, VGS=0V	600	-	-	V
Gate threshold voltage	$V_{GS(th)}$	ID=250uA, VDS=VGS	2.0	-	4.0	V
Drain-source cut-off current	Idss	V <sub>DS</sub> =600V, V <sub>GS</sub> =0V	-	-	1	uA
Gate leakage current	Igss	$V_{DS}=0V$ , $V_{GS}=\pm 30V$	-	-	±100	nA
Drain-source on-resistance	RDS(on)	Vgs=10V, Id=6.0A	-	0.55	0.65	Ω
Forward transfer conductance $\textcircled{4}$	<b>g</b> fs	VDS=10V, ID=6.0A	-	10	-	S
Input capacitance	Ciss	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V f=1MHz	-	2162	2882	
Output capacitance	Coss		-	183	244	pF
Reverse transfer capacitance	Crss		-	14.6	19.4	
Turn-on delay time	td(on)		-	30	-	
Rise time	tr	V <sub>DD</sub> =300V, I <sub>D</sub> =12A R <sub>G</sub> =25Ω ③④	-	85	-	
Turn-off delay time	td(off)		-	140	-	20
Fall time	tſ		-	90	-	ns
Total gate charge	Qg	V <sub>DS</sub> =480V, V <sub>GS</sub> =10V I <sub>D</sub> =12A ③④	-	41	63	
Gate-source charge	Qgs		-	13	-	nC
Gate-drain charge	$Q_{gd}$		-	10.5	-	

#### Source-Drain Diode Ratings and Characteristics (Tc=25°C unless otherwise noted)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Source current (DC)	Is	Integral reverse diode	-	-	12	~
Source current (Pulsed) ①	Isм	in the MOSFET	-	-	48	A
Forward voltage ④	Vsd	V <sub>GS</sub> =0V, I <sub>S</sub> =9.0A	-	-	1.4	V
Reverse recovery time	trr	Is=9.0A, V <sub>GS</sub> =0V	-	510	-	ns
Reverse recovery charge	Qrr	dIF/dt=100A/us	-	4.3	-	uC

Note ;

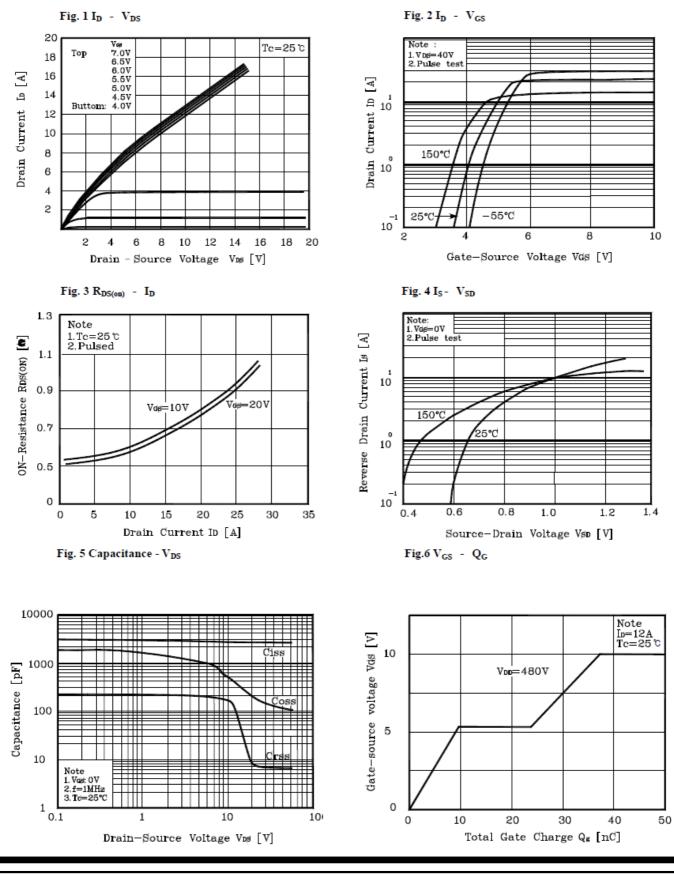
① Repetitive rating : Pulse width limited by maximum junction temperature

<sup>(2)</sup> L=7mH, IAS=12A, VDD=50V, RG=25 $\Omega$ , Starting TJ=25<sup>°</sup>C

③ Pulse Test : Pulse width≤300us, Duty cycle≤2%

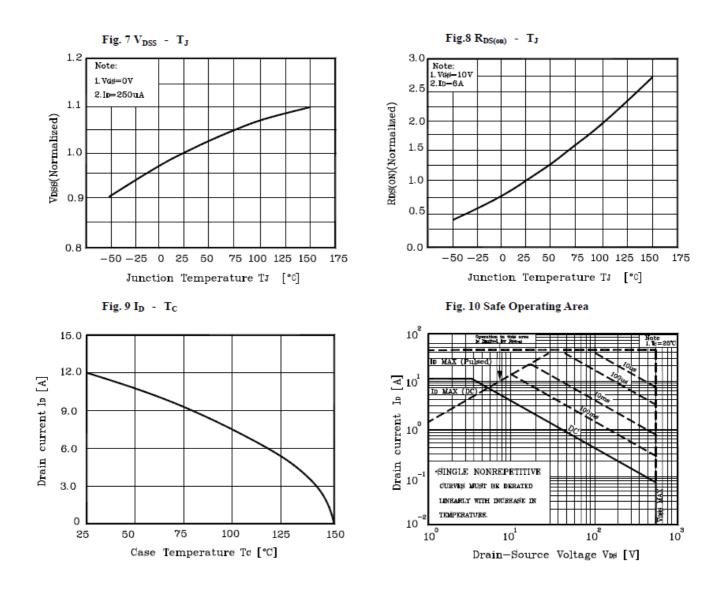
④ Essentially independent of operating temperature





## **KMK1260F**

#### **Electrical Characteristic Curves**



### **KMK1260F**

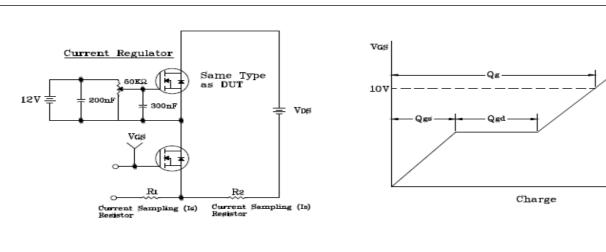
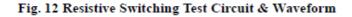
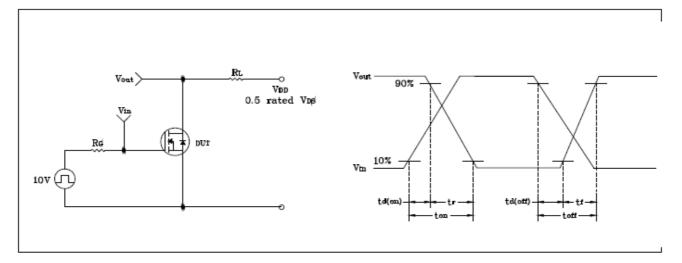
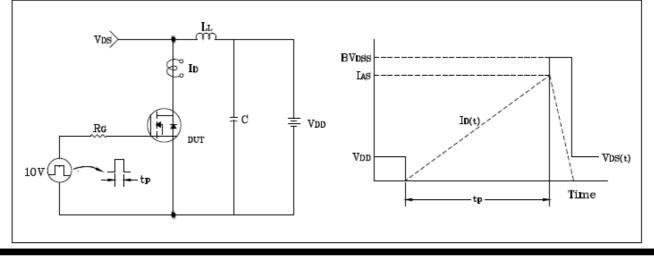


Fig. 11 Gate Charge Test Circuit & Waveform

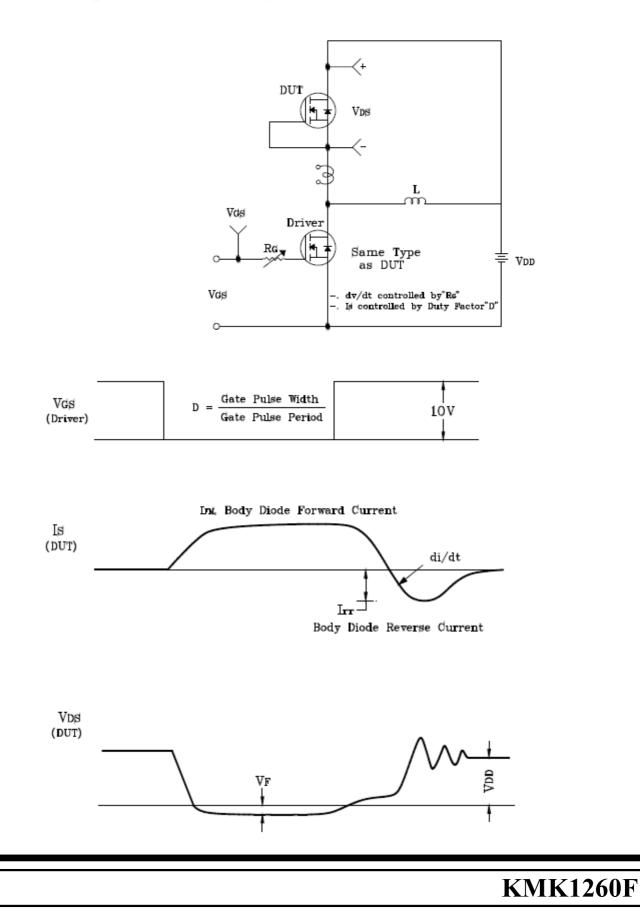








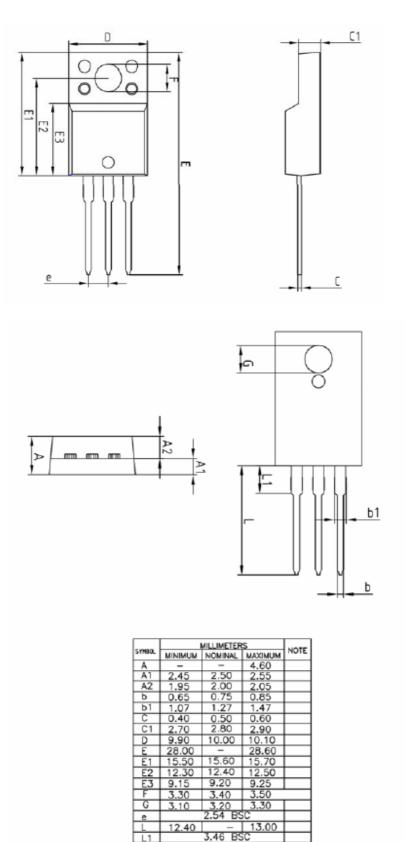
#### KMK1260F



#### Fig. 14 Diode Reverse Recovery Time Test Circuit & Waveform

### **Outline Dimension**

unit: mm



### KMK1260F

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