

**KTD123S** 

**NPN Silicon Transistor** 

#### **Features**

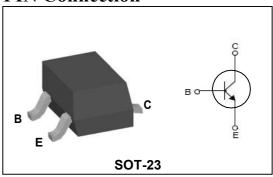
- Low saturation medium current application
- Extremely low collector saturation voltage
- Suitable for low voltage large current drivers
- High DC current gain and large current capability
- Low on resistance :  $R_{ON}=0.6\land(Max.)$  ( $I_B=1mA$ )

### **Ordering Information**

Type No.	Marking	Package Code
KTD123S	<u>123</u> □•	SOT-23
	1 2	501-23

①Device Code ② Year&Week Code •Dalian

# **PIN Connection**



## Absolute maximum ratings

(Ta=25 C)

Characteristic	Symbol	Ratings	Unit
Collector-Base voltage	$V_{CBO}$	20	V
Collector-Emitter voltage	$V_{CEO}$	15	V
Emitter-Base voltage	$V_{EBO}$	6.5	V
Collector current	$I_{C}$	1	Α
Collector dissipation	P <sub>C</sub>	350	mW
Junction temperature	$T_{j}$	150	°C
Storage temperature	$T_{stg}$	-55~150	°C

<sup>\* :</sup> Package mounted on 99.5% alumina 10×8×0.1mm

#### **Electrical Characteristics**

(Ta=25 C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Collector-Base breakdown voltage	BV <sub>CBO</sub>	I <sub>C</sub> =50μA, I <sub>E</sub> =0	20	-	-	٧
Collector-Emitter breakdown voltage	BV <sub>CEO</sub>	$I_C=1$ mA, $I_B=0$	15	-	ı	٧
Emitter-Base breakdown voltage	BV <sub>EBO</sub>	I <sub>E</sub> =50μA, I <sub>C</sub> =0	6.5	-	1	٧
Collector cut-off current	$I_{CBO}$	V <sub>CB</sub> =20V, I <sub>E</sub> =0	-	-	0.1	μΑ
Emitter cut-off current	I <sub>EBO</sub>	V <sub>EB</sub> =6V, I <sub>C</sub> =0	-	-	0.1	μΑ
DC current gain	h <sub>FE</sub>	V <sub>CE</sub> =1V, I <sub>C</sub> =100mA	150	-	-	-
Collector-Emitter saturation voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> =500mA, I <sub>B</sub> =50mA	-	0.1	0.3	V
Transistor frequency	f⊤	$V_{CE}=5V$ , $I_{C}=50$ mA	-	260	-	MHz
Collector output capacitance	C <sub>ob</sub>	V <sub>CB</sub> =10V, I <sub>E</sub> =0, f=1MHz	-	5	-	pF
On resistance	R <sub>ON</sub>	f=1KHz, I <sub>B</sub> =1mA, V <sub>IN</sub> =0.3V	-	0.6	-	^

### **Electrical Characteristic Curves**

Fig. 1 P<sub>C</sub> - T<sub>a</sub>

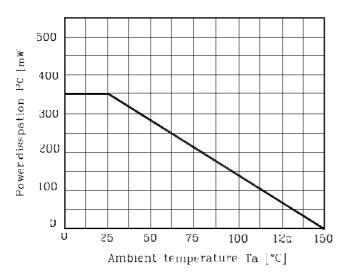


Fig. 2 C<sub>Ob-</sub>V<sub>CB</sub>

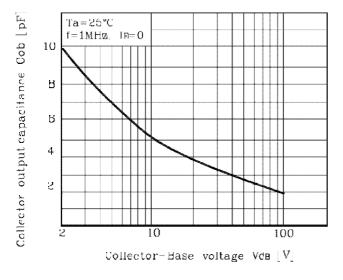


Fig. 5 Ron-IB

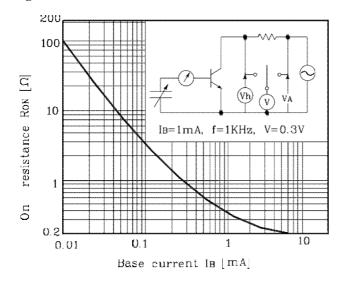
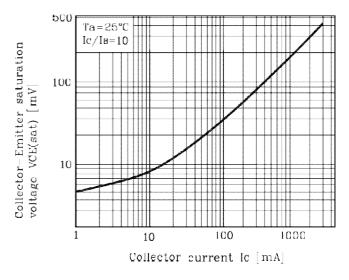
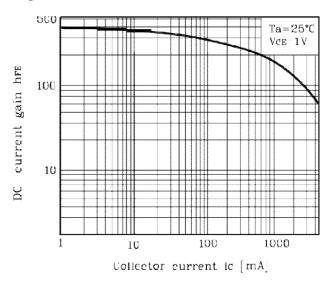


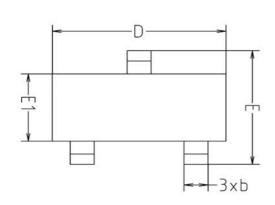
Fig. 2  $V_{CE(sat)}$ - $I_C$ 

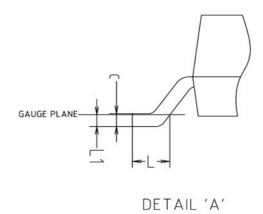


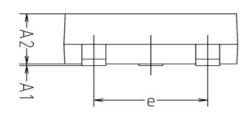
 $Fig.\ 4h_{FE}\text{-}I_{C}$ 

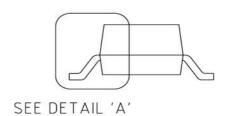


## **Outline Dimension**



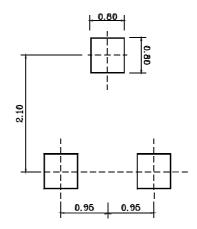






SYMBOL	MILLIMETERS			NOTE
	MINIMUM	NOMINAL	MAXIMUM	NOTE
Α1	0.00	-	0.10	
Α2	0.82	-	1.02	
Ь	0.39	0.42	0.45	
C	0.09	0.12	0.15	
D	2.80	2.90	3.00	
E	2.20	2.40	2.60	
E1	1.20	1.30	1.40	
е	1.90BSC			
L	0.20	-		
L1		0.12BSC		

# **\*\*Recommend PCB solder land [Unit: mm]**



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